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PATENT NUMBER and
ISSUE DATE

6667513

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10018179	05/03/2002	257	260	2811	Hoai Pham

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**CONTINUING DATA VERIFIED: H7

THIS APPLICATION IS A 371 OF PCT/FR00/01537 06/05/2000

YES

** FOREIGN APPLICATIONS VERIFIED: H7

FRANCE 9907391 06/11/1999

YES

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no	
35 USC 113 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged: Examiner's initials		Hoai Pham	H7 5310-03900

TITLE : Semiconductor device with compensated threshold voltage and method for making same

U.S. DEPT. OF COM. PAT. & T. I. PTO-435 (Rev. 12-87)

11/12/03 Formal Drawings (2 sheets) 351 5/3/02

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
09/08/03		HOAI PHAM	
ISSUE FEE <i>160</i>		Assistant Examiner	Total Claims 20 Print Claims for O.G. 1
Amount Due <i>1300</i>	Date Paid <i>10-6-03</i>	Primary Examiner	DRAWING
SU			Sheets Drawg. 2 Figs. Drawg. 3 Print Fig. 1
<input type="checkbox"/> TERMINAL		PREPARED FOR ISSUE	
<input type="checkbox"/> DISCLAIMER		Application Examiner	
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